

PALM INTRANET

Day : Friday
 Date: 6/18/2004
 Time: 07:45:00

Inventor Name Search Result

Your Search was:

Last Name = MIYAMOTO

First Name = SHOICHI

Application#	Patent#	Status	Date Filed	Title	Inventor Name 26
<u>10682124</u>	Not Issued	030	10/10/2003	SEMICONDUCTOR DEVICE FORMED ON INSULATING LAYER AND METHOD OF MANUFACTURING THE SAME	MIYAMOTO, SHOICHI
<u>10431473</u>	Not Issued	071	05/08/2003	A SEMICONDUCTOR DEVICE HAVING SPACES POSITIONED NEAR ISOLATION TRENCHES	MIYAMOTO, SHOICHI
<u>10336758</u>	<u>6653656</u>	150	01/06/2003	SEMICONDUCTOR DEVICE FORMED ON INSULATING LAYER AND METHOD OF MANUFACTURING THE SAME	MIYAMOTO, SHOICHI
<u>10315712</u>	Not Issued	030	12/10/2002	CODE DIVISION MULTIPLE ACCESS COMMUNICATION SYSTEM, AND A BASE STATION CONTROL DEVICE AND A BASE STATION THEREOF	MIYAMOTO, SHOICHI
<u>10120485</u>	Not Issued	094	04/12/2002	SEMICONDUCTOR DEVICE, METHOD OF MANUFACTURING SAME AND METHOD OF DESIGNING SAME	MIYAMOTO, SHOICHI
<u>09999388</u>	Not Issued	030	10/31/2001	METHOD AND APPARATUS FOR ASSIGNING WALSH CODES	MIYAMOTO, SHOICHI
<u>09989152</u>	<u>6461935</u>	150	11/21/2001	METHOD OF MANUFACTURING TRENCH-SHAPED ISOLATOR	MIYAMOTO, SHOICHI
<u>09919880</u>	Not Issued	161	08/02/2001	GEAR-MESH TYPE AUTOMATIC	MIYAMOTO, SHOICHI

TRANSMISSION SYSTEM					
<u>09814241</u>	Not Issued	071	03/21/2001	BASE STATION CONTROL EQUIPMENT, RADIO BASE STATION EQUIPMENT, RADIO TERMINAL EQUIPMENT, AND MOBILE COMMUNICATION SYSTEM	MIYAMOTO, SHOICHI
<u>09765439</u>	<u>6475114</u>	150	01/22/2001	GEAR-MESH TYPE AUTOMATIC TRANSMISSION SYSTEM	MIYAMOTO, SHOICHI
<u>09677848</u>	<u>6509583</u>	150	10/03/2000	SEMICONDUCTOR DEVICE FORMED ON INSULATING LAYER AND METHOD OF MANUFACTURING THE SAME	MIYAMOTO, SHOICHI
<u>09628167</u>	<u>6379279</u>	150	07/28/2000	GEAR TYPE AUTOMATIC TRANSMISSION UNIT AND METHOD FOR CONTROLLING THE SAME	MIYAMOTO, SHOICHI
<u>09503705</u>	<u>6631142</u>	150	02/14/2000	DELAY CORRECTION SYSTEM FOR WIRELESS TELEPHONE SYSTEM	MIYAMOTO, SHOICHI
<u>09494352</u>	<u>6271065</u>	150	01/31/2000	METHOD DIRECTED TO THE MANUFACTURE OF AN SOI DEVICE	MIYAMOTO, SHOICHI
<u>09371027</u>	<u>6372599</u>	150	08/10/1999	SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE SAME	MIYAMOTO , SHOICHI
<u>09346726</u>	<u>6586802</u>	150	07/02/1999	SEMICONDUCTOR DEVICE	MIYAMOTO , SHOICHI
<u>09231548</u>	<u>6144072</u>	150	01/15/1999	SEMICONDUCTOR DEVICE FORMED ON INSULATING LAYER AND METHOD OF MANUFACTURING THE SAME	MIYAMOTO , SHOICHI
<u>09221121</u>	<u>6225148</u>	150	12/28/1998	METHOD OF FABRICATING SEMICONDUCTOR DEVICE	MIYAMOTO , SHOICHI
<u>09197705</u>	<u>6603174</u>	150	11/23/1998	SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF	MIYAMOTO , SHOICHI
<u>08996811</u>	<u>5965914</u>	150	12/23/1997	A THIN FILM TRANSISTOR HAVING A BRANCHED GATE AND CHANNEL	MIYAMOTO , SHOICHI

<u>08939443</u>	<u>6127874</u>	150	09/29/1997	SKEW ADJUSTABLE IC AND A METHOD FOR DESIGNING THE SAME	MIYAMOTO , SHOICHI
<u>08794504</u>	<u>5905286</u>	150	02/04/1997	SEMICONDUCTOR DEVICE	MIYAMOTO , SHOICHI
<u>08671542</u>	<u>6064090</u>	150	06/27/1996	SEMICONDUCTOR DEVICE HAVING A PORTION OF GATE ELECTRODE FORMED ON AN INSULATING SUBSTRATE	MIYAMOTO , SHOICHI
<u>08461777</u>	Not Issued	166	06/05/1995	SEMICONDUCTOR DEVICE FORMED ON INSULATING LAYER AND METHOD OF MANUFACTURING THE SAME	MIYAMOTO , SHOICHI
<u>06527449</u>	<u>4530826</u>	150	08/29/1983	METHOD FOR RECOVERING AND UTILIZING WASTE HEAT	MIYAMOTO , SHOICHI
<u>06326780</u>	Not Issued	164	12/02/1981	METHOD FOR RECOVERING AND UTILIZING WASTE HEAT	MIYAMOTO , SHOICHI

Inventor Search Completed: No Records to Display.

Search Another:
Inventor

Last Name

miyamoto

First Name

shoichi

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | Home page